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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTENTION: HONORABLE
Commissioner for Patents

P.O. Box 1450

In re the Patent of: **TAWARAGI, Yuji**

P.T.O. Confirmation No.: **8503**

Patent No. **7,366,067**

Issued: **April 29, 2008**

For: **RECORDING CLOCK SIGNAL GENERATING APPARATUS AND RECORDING CLOCK SIGNAL GENERATING METHOD FOR INFORMATION RECORDING DEVICE (AS AMENDED)**

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Mail Stop DAC
June 26, 2008

Sir:

The undersigned requests that a Certificate of Correction be issued for the above-identified patent as indicated on the attached Form PTO-1050.

This request is being made in order to correct **column 12, line 16 of claim 7, "base" should read --phase--**. Copies of pages 2 through 5 of the Amendment to the claims filed September 17, 2007, and Column 12 of the Letters Patent No. 7,366,067 are enclosed which indicate the correct information. It is respectfully submitted that no new matter has been added.

Since this error is a Patent and Trademark Office printing error, it is respectfully submitted that no fee is required.

Respectfully submitted,

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Enclosures: PTO-1050, Letters Patent Column 12 and pages 2-5 of Amendment filed 9/17/07.

UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO.: **7,366,067**
APPLICATION NO.: **10/615,386**
DATED : **April 29, 2008**
INVENTOR(S): **TAWARAGI, Yuji**

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 12, Claim 7

On line 16, **"base"**

should read:

On line 16, **--phase--**

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the phase signal adjustment signal S_{CNT}, and the wobble signal S_{WB} synchronized to the pre-pit signal S_{PD} may be generated in any way.

Further the configuration for monitoring an erroneous phase difference indicated by the phase adjustment signal S_{CNT} is not limited to that in which the phase adjustment signals S_{CNT} in the past are fetched to calculate the average value to monitor any error, but method may be employed including those, for instance which an erroneous phase difference is monitored by setting a reference value separately and comparing the given phase difference to the reference value, or in which the difference from the phase adjustment signal S_{CNT} just ahead is monitored for checking any rapid change in the difference from the phase adjustment signal S_{CNT} just ahead.

What is claimed is:

1. A recording clock signal generating apparatus located in an information recording device for recording information in a recording medium in which a wobbled information recording track and pre-pit formed thereon, said apparatus comprising:

a wobble signal detecting section for detecting a wobble signal;

a pre-pit signal detecting section for detecting a pre-pit signal;

a phase comparing section for comparing a phase of said wobble signal to that of said pre-pit signal and outputting the phase difference;

a determining section for determining whether the pre-pit signal is generated by erroneous detection of the pre-pit or not;

a phase-shifting section for shifting a phase of said wobbled signal based on said phase difference only when the determining section determines that the pre-pit signal is not generated by the erroneous detection; and

a clock signal generating section for generating a recording clock signal based on said phase-shifted wobble signal,

wherein the determining section compares the phase difference with a predetermined threshold, does not determine that the pre-pit signal is generated by the erroneous detection when the phase difference is within the threshold and determines that the pre-pit signal is generated by the erroneous detection when the phase difference is out of the range of the threshold.

2. The recording clock signal generating apparatus according to claim 1 further comprising:

a control section for controlling said phase-sifting section when said phase difference is within a threshold width value set for the phase differences in the past.

3. The recording clock signal generating apparatus according to claim 2, wherein said control section comprises a history data storing section for storing therein history data for said phase differences and a phase comparing section for comparing a given phase difference to the history data for the phase differences stored in said history data storing section.

4. The recording clock signal generating apparatus according to claim 3, wherein said history data storing section outputs an average value for said phase differences in the past stored therein.

5. The recording clock signal generating section according to claim 3, wherein said phase difference comparing section sets a control signal outputted to said phase-shifting section at a high level only when the threshold width value relative to the history data of said phase differences in the past is set and said phase difference is within the threshold width value.

6. The recording clock signal generating apparatus according to claim 1, further comprising a control unit for controlling said phase-shifting section only when said phase difference is within a fixed width value set relative to the phase difference.

7. A recording clock signal generating method for recording information in a recording medium in which a wobbled information recording track and a pre-pit formed thereon, said method comprising the steps of:

detecting a wobble signal;

detecting a pre-pit signal;

comparing a phase of said wobble signal to that of said pre-pit signal and outputting the phase difference;

determining whether the pre-pit signal is generated by erroneous detection of the pre-pit or not;

shifting a phase of said wobble signal based on said phase difference only when it is determined that the pre-pit signal is not generated by the erroneous detection; and a clock signal generating step of generating a recording clock signal based on said phase-shifted wobble signal,

wherein the determining step compares the ~~base~~ phase difference with a predetermined threshold, does not determine that the pre-pit signal is generated by the erroneous detection when the phase difference is within the threshold and determines that the pre-pit signal is generated by the erroneous detection when the phase difference is out of the range of the threshold.

* * * * *

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

Claim 1 (Currently Amended): A recording clock signal generating apparatus located in an information recording device for recording information in a recording medium in which a wobbled information recording track and pre-pit formed thereon, said apparatus comprising:

a wobble signal detecting section for detecting a wobble signal;

a pre-pit signal detecting section for detecting a pre-pit signal;

a phase comparing section for comparing a phase of said wobble signal to that of said pre-pit signal and outputting the phase difference;

a determining section for determining whether the pre-pit signal is generated by erroneous detection of the pre-pit or not;

a phase-shifting section for shifting a phase of said wobbled signal based on said phase difference only when the determining section determines that the pre-pit signal is not generated by the erroneous detection; and

a clock signal generating section for generating a recording clock signal based on said phase-shifted wobble signal,

wherein the determining section compares the phase difference with a predetermined

threshold, does not determine that the pre-pit signal is generated by the erroneous detection when the phase difference is within the threshold and determines that the pre-pit signal is generated by the erroneous detection when the phase difference is higher out of the range of the threshold.

Claim 2 (Original): The recording clock signal generating apparatus according to claim 1, further comprising:

a control section for controlling said phase-sifting section when said phase difference is within a threshold width value set for the phase differences in the past.

Claim 3 (Original): The recording clock signal generating apparatus according to claim 2, wherein said control section comprises a history data storing section for storing therein history data for said phase differences and a phase comparing section for comparing a given phase difference to the history data for the phase differences stored in said history data storing section.

Claim 4 (Original): The recording clock signal generating apparatus according to claim 3, wherein said history data storing section outputs an average value for said phase differences in the past stored therein.

Claim 5 (Original): The recording clock signal generating section according to claim 3, wherein said phase difference comparing section sets a control signal outputted to said phase-shifting

section at a high level only when the threshold width value relative to the history data of said phase differences in the past is set and said phase difference is within the threshold width value.

Claim 6 (Original): The recording clock signal generating apparatus according to claim 1, further comprising a control unit for controlling said phase-shifting section only when said phase difference is within a fixed width value set relative to the phase difference.

Claim 7 (Currently Amended): A recording clock signal generating method for recording information in a recording medium in which a wobbled information recording track and a pre-pit formed thereon, said method comprising the steps of:

detecting a wobble signal;

detecting a pre-pit signal;

comparing a phase of said wobble signal to that of said pre-pit signal and outputting the phase difference;

determining whether the pre-pit signal is generated by erroneous detection of the pre-pit or not;

shifting a phase of said wobble signal based on said phase difference only when it is determined that the pre-pit signal is not generated by the erroneous detection; and

a clock signal generating step of generating a recording clock signal based on said phase-shifted wobble signal.

wherein the determining step compares the phase difference with a predetermined threshold, does not determine that the pre-pit signal is generated by the erroneous detection when the phase difference is within the threshold and determines that the pre-pit signal is generated by the erroneous detection when the phase difference is higher out of the range of the threshold.

Claim 8 (Canceled).